

**National Institute of Technology Puducherry (Round 1)**

<b>PG Program</b>	<b>Group</b>	<b>Category</b>	<b>Max GATE Score</b>	<b>Min GATE Score</b>
Structural Engineering - Resilience and Sustainability	G1	EWS	322	322
Power Electronics	G1	EWS	328	328
Computer Science & Engineering	G1	EWS	472	472
Structural Engineering - Resilience and Sustainability	G1	OBC-NCL	396	355
Power Electronics	G1	SC	345	345
Computer Science & Engineering	G1	OBC-NCL	511	501
VLSI Design	G1	OPEN	524	454
Power Electronics	G1	ST	253	253
Power Electronics	G1	OBC-NCL	363	363
VLSI Design	G1	SC	346	320
Power Electronics	G1	OPEN	442	371
Digital Manufacturing and Smart Factories	G1	SC	265	265
Structural Engineering - Resilience and Sustainability	G1	OPEN	707	401
Computer Science & Engineering	G1	OPEN	595	540
Digital Manufacturing and Smart Factories	G1	OBC-NCL	322	322
Structural Engineering - Resilience and Sustainability	G1	SC	333	333
Structural Engineering - Resilience and Sustainability	G1	ST	259	259
Computer Science & Engineering	G1	SC	390	381
VLSI Design	G1	OBC-NCL	400	395
VLSI Design	G1	EWS	385	385

**National Institute of Technology Puducherry (Round 2)**

<b>PG Program</b>	<b>Group</b>	<b>Category</b>	<b>Max GATE Score</b>	<b>Min GATE Score</b>
Structural Engineering - Resilience and Sustainability	Group 1	OBC-NCL	355	331
Structural Engineering - Resilience and Sustainability	Group 1	OPEN	396	355
Digital Manufacturing and Smart Factories	Group 1	SC	241	241
Power Electronics	Group 1	OBC-NCL	354	350
Power Electronics	Group 1	OPEN	358	354
VLSI Design	Group 1	SC	305	300
VLSI Design	Group 1	OBC-NCL	395	390
VLSI Design	Group 1	EWS	360	360
VLSI Design	Group 1	OPEN	449	438
Computer Science & Engineering	Group 1	OBC-NCL	463	463
Computer Science & Engineering	Group 1	EWS	452	452
Computer Science & Engineering	Group 1	OPEN	522	511
Computer Science & Engineering	Group 1	SC	360	360

**National Institute of Technology Puducherry (Round 3)**

<b>PG Program</b>	<b>Group</b>	<b>Category</b>	<b>Max GATE Score</b>	<b>Min GATE Score</b>
Structural Engineering - Resilience and Sustainability	Group 1	OBC-NCL	326	326
Structural Engineering - Resilience and Sustainability	Group 1	OPEN	355	355
Digital Manufacturing and Smart Factories	Group 1	SC	236	236
Power Electronics	Group 1	OBC-NCL	345	345
VLSI Design	Group 1	OBC-NCL	375	375
Computer Science & Engineering	Group 1	OBC-NCL	443	443
Computer Science & Engineering	Group 1	EWS	448	448

Computer Science & Engineering	Group 1	OPEN	510	510
Computer Science & Engineering	Group 1	SC	342	342

**National Institute of Technology Puducherry (Special Round 1)**

<b>PG Program</b>	<b>Group</b>	<b>Category</b>	<b>Max GATE Score</b>	<b>Min GATE Score</b>
Power Electronics	Group 1	SC	275	275
VLSI Design	Group 1	OBC-NCL	365	360
VLSI Design	Group 1	OPEN	414	385
VLSI Design	Group 1	SC	284	284
Computer Science & Engineering	Group 1	OBC-NCL	423	423
Computer Science & Engineering	Group 1	EWS	396	396
Computer Science & Engineering	Group 1	OPEN	482	454
Computer Science & Engineering	Group 1	SC	335	335
Structural Engineering - Resilience and Sustainability	Group 1	OPEN	399	355
Structural Engineering - Resilience and Sustainability	Group 1	ST	259	259

**National Institute of Technology Puducherry (Special Round 2)**

<b>PG Program</b>	<b>Group</b>	<b>Category</b>	<b>Max GATE Score</b>	<b>Min GATE Score</b>
Power Electronics	Group 1	SC	257	257
VLSI Design	Group 1	OBC-NCL	360	360
VLSI Design	Group 1	OPEN	375	375
Computer Science & Engineering	Group 1	SC	320	320
Computer Science & Engineering	Group 1	EWS	364	364
Computer Science & Engineering	Group 1	OPEN	456	456

**National Institute of Technology Puducherry (National Spot Round)**

<b>PG Program</b>	<b>Group</b>	<b>Category</b>	<b>Max GATE Score</b>	<b>Min GATE Score</b>
Power Electronics	Group 1	OBC-NCL	332	332

VLSI Design	Group 1	OBC-NCL	355	330
VLSI Design	Group 1	OPEN	365	365
Computer Science & Engineering	Group 1	SC	291	291
Computer Science & Engineering	Group 1	OBC-NCL	364	360
Computer Science & Engineering	Group 1	OPEN	443	415