



**Admission Test for admission to**  
**PhD Programme in Applied Physics (Electrical Engineering)**  
**Department of Applied Physics, University of Calcutta**  
**RET 2025**

**Notification for admission in the PhD Programme (RET2025) in Applied Physics (Electrical Engineering)**

**Total No. of Seats in Electrical Engineering: 20 (twenty)** (Reservation Policy is as per CU Norms)

**Research areas where applications are invited:**

1. AI, Robotics and control systems.
2. Power system and related communications.
3. Power Electronics Applications and renewable Energy.
4. Biomedical Signal Analysis/ intelligent health monitoring.

**Eligibility Criteria:**

- Masters' degree in Engineering/Technology or its equivalent with at least 65% marks in aggregate or its equivalent Grade-B in UGC -7 points scale.
- First class in Bachelor's degree in Engineering / Technology and Master's degree in Pure Science with at least 65% marks in aggregate. However, they have to appear and qualify in an Entrance test and have to go through other formalities as per CU PhD regulation 2016.

For further detailed information, applicants may go through University of Calcutta (Regulation for the Degree of Doctor of Philosophy, Ph.D., notification No CSR/60/16), Regulations 2016, in the University website: [www.caluniv.ac.in](http://www.caluniv.ac.in)

**Entrance Test and Selection Criteria** (Applicable to candidates as mentioned in C.U. Ph.D. rule 2016):

- The Ph.D. research entrance test (RET) will be written test for all eligible candidates. The examination will be of 1 (one) hour duration, at Department of Applied Physics, University of Calcutta, 92 APC Road, Kolkata-700009.
- The questions of the Entrance Test will, in general, cover topics of Electrical Engineering, Instrumentation Engineering, basics of Physics, Electronics, Communication Systems and Computer Science.
- Qualifying marks for entrance test will be 65%. The qualified candidates will have to appear for an online presentation on specific research proposal for final selection.
- The final list of selected candidates will be prepared based on the merit of the research proposal, interview performance and falling within the scope of research of Electrical Engineering Faculties of this department.
- Declaration of names of selected candidates to be communicated electronically (After the approval from Hon'ble Vice Chancellor).



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**Important dates**

Date of Advertisement in CU website	06.03.2025
Last date of submission of duly filled-in admission Form (in physical hard copy format)	24.03.2025
Publication of List of Candidates appearing for Entrance test (in CU website)	24.03.2025
Date, time of online written test	09.04.2025 from 11am <i>in Physical mode</i>
Publication of List of Candidates eligible to appear for an Interview (in CU website)	23.04.2025
Date, time Interview followed by a presentation on research proposal	30.04.2025 from 11am <i>in Physical mode</i>
Publication of List of Candidates for Enrollment in CU website (subject to the approval of Hon'ble Vice Chancellor)	19.05.2025

**Note: All the dates may be modified based on the approval from Hon'ble Vice Chancellor**

Hard copy applications with all documents to be submitted between 11:30am -4:30 pm at:

Office, Department of Applied Physics, University of Calcutta, Rashbehari Siksha Prangan, 92, APC Road, Kolkata-700009

Contact No: 033-23508386 (Ext-461)

Candidates residing outside West Bengal / Physically challenged candidates may courier their application documents to: Head, Department of Applied Physics, University of Calcutta, Rashbehari Siksha Prangan, 92, APC Road, Kolkata-700009.

**Deposition of application fees: INR100/ (one hundred only)**

The candidates shall online deposit the application fees to the following account number:

online through SBI Collect [<https://www.onlinesbi.com/sbicollect/>]

After depositing the fees, the challan / transaction reference number to be mentioned in the application form and a print copy of the challan to be enclosed along with the application form.

**Documents to be enclosed along with the filled-in application form:**

1. Self-attested copies of marksheets of 10<sup>th</sup>, 10+2 level, BE/BTech, ME/ MTech plus other qualifications, if any.
2. Evidence of research work undertaken/ papers published in journals/conferences out of research/ project work carried out earlier, if any.
3. A one-page abstract of research proposal.



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**Application Form**

Name (in block letters)	
Date of Birth	
Father's Name	
Sex (Male/Female /Third Gender)	
Marital Status	
Whether SC/ST/OBC/ Physically Challenged (if yes, certificate to be enclosed)	
Nationality	
Address for Communication	
Phone Number	
E-mail ID	
Employed/unemployed	
Designation, if employed	
Name and address of employer, if employed	
Whether qualified in NET/SLET/equivalent	
Proposed area of Research (Electrical Engg.) Please enclose a one-page abstract of research proposal in a separate sheet.	
Fees deposit transaction reference number and Date	
Whether enrolled/ registered/ submitted/ awarded for PhD degree elsewhere	

Full Signature of the applicant with date



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**Academic Qualifications:**

Name of the Examinations	Year of passing	Board /University	Major Subjects	Div./ Class	% of Marks	Equivalent grade if any
Madhyamik/equiv. (10)						
Higher Secondary/ equiv. (10+2)						
B.Sc.(Hons.) / Diploma						
M.Sc.						
B.Tech./ BE						
M.Tech./ME						

Full Signature of the applicant with date